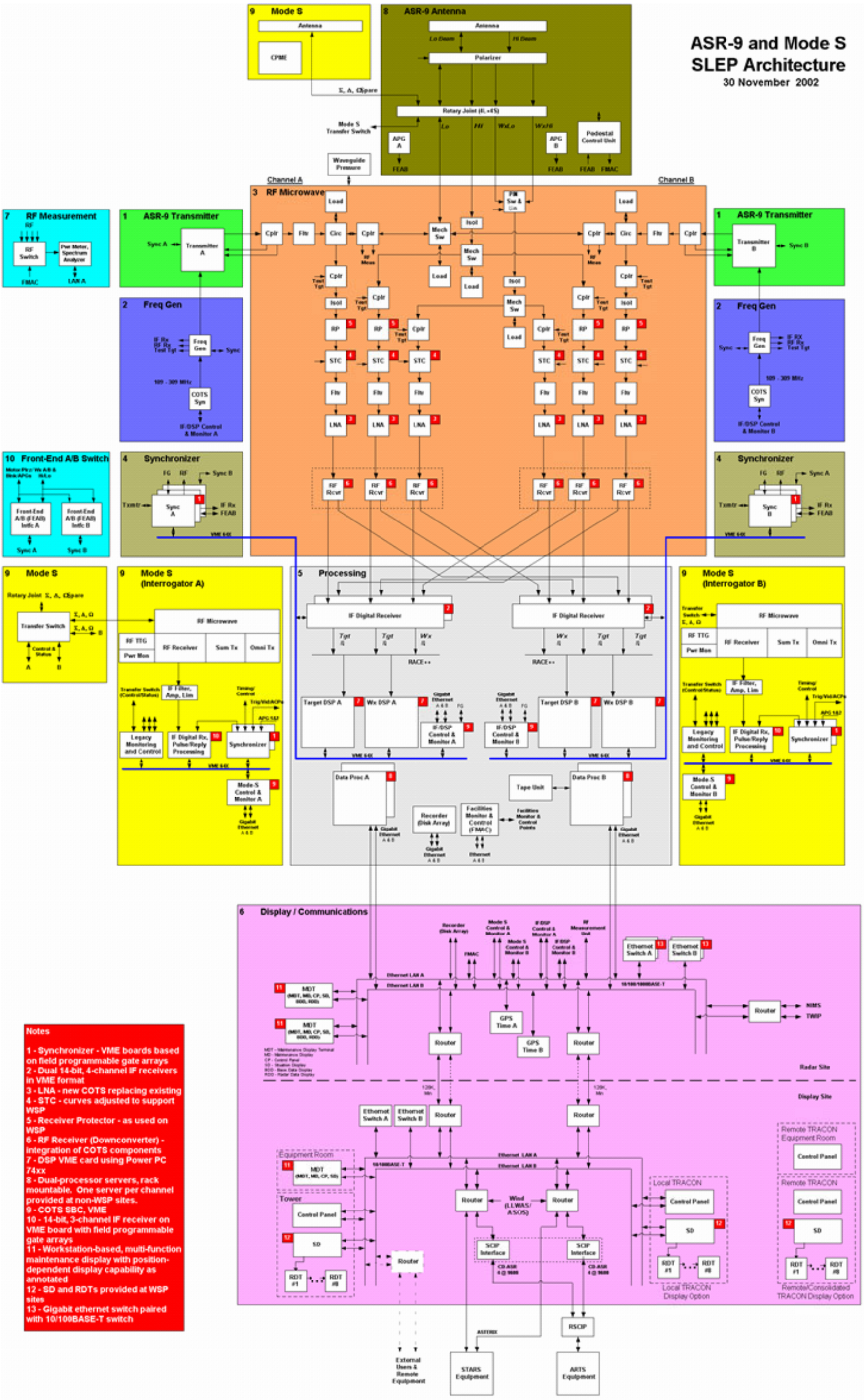
ASR-9 and Mode S SLEP Architecture

30 November 2002



Notes

1 - Synchronizer - VME boards based on field programmable gate arrays
2 - Dual 14-bit, 4-channel IF receivers in VME format
3 - LNA - new COTS replacing existing
4 - STC - curves adjusted to support WSP
5 - Receiver Protector - as used on WSP
6 - RF Receiver (Downconverter) - integration of COTS components
7 - DSP VME card using Power PC 74xx
8 - Dual-processor servers, rack mountable. One server per channel provided at non-WSP sites.
9 - COTS SBC, VME
10 - 14-bit, 3-channel IF receiver on VME board with field programmable gate arrays
11 - Workstation-based, multi-function maintenance display with position-dependent display capability as annotated
12 - SD and RDTs provided at WSP sites
13 - Gigabit ethernet switch paired with 10/100BASE-T switch

SLEP Notional Architecture HANDOUT 04dec2002.vsd